

EMBEDDED REAL-TIME SYSTEMS

*A Specification and
Design Methodology*



• **Jean Paul Calvez**



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Embedded Real Time Systems A Specification And Design Methodology

**Cliff B. Jones, Zhiming Liu, Jim
Woodcock**



Embedded Real Time Systems A Specification And Design Methodology:

Specification and Design Methodology for Real-Time Embedded Systems Randall S. Janka, 2012-12-06 Specification and design methodology has seen significant growth as a research area over the last decade tracking but lagging behind VLSI design technology in general and the CAD industry in particular The commercial rush to market tries to leverage existing technology which fuels CAD design tool development Paralleling this is very active basic and applied research to investigate and move forward rational and effective methodologies for accomplishing digital design especially in the field of hardware software codesign It is this close relationship between industry and academia that makes close cooperation between researchers and practitioners so important and monographs like this that combine both abstract concept and pragmatic implementation deftly bridge this often gaping chasm It was at the IEEE ACM Eighth International Symposium on Hardware Software Codesign where I met the author of this monograph Dr Randall Janka who was presenting some of his recent dissertation research results on specification and design methodology or as he has so succinctly defined this sometimes ambiguous concept the tools and rules Where so many codesign researchers are trying to prove out different aspects of codesign and using toy applications to do so Dr Janka had developed a complete specification and design methodology and prototyped the infrastructure and proven its viability utility and effectiveness using a demanding real world application of a real time synthetic aperture radar imaging processor that was implemented with embedded parallel processors

Advanced Techniques for Embedded Systems Design and Test Juan C. López, Román Hermida, Walter Geisselhardt, 2013-03-09 As electronic technology reaches the point where complex systems can be integrated on a single chip and higher degrees of performance can be achieved at lower costs designers must devise new ways to undertake the laborious task of coping with the numerous and non trivial problems that arise during the conception of such systems On the other hand shorter design cycles so that electronic products can fit into shrinking market windows put companies and consequently designers under pressure in a race to obtain reliable products in the minimum period of time New methodologies supported by automation and abstraction have appeared which have been crucial in making it possible for system designers to take over the traditional electronic design process and embedded systems is one of the fields that these methodologies are mainly targeting The inherent complexity of these systems with hardware and software components that usually execute concurrently and the very tight cost and performance constraints make them specially suitable to introduce higher levels of abstraction and automation so as to allow the designer to better tackle the many problems that appear during their design Advanced Techniques for Embedded Systems Design and Test is a comprehensive book presenting recent developments in methodologies and tools for the specification synthesis verification and test of embedded systems characterized by the use of high level languages as a road to productivity Each specific part of the design process from specification through to test is looked at with a constant emphasis on behavioral methodologies Advanced Techniques for

Embedded Systems Design and Test is essential reading for all researchers in the design and test communities as well as system designers and CAD tools developers *Design of Hardware/Software Embedded Systems* Eugenio Villar Bonet, 2001

Este libro presenta los desafíos planteados por las nuevas y sumamente poderosas tecnologías de integración de sistemas electrónicos que están en la base de los cambios sociales hacia lo que llaman la Sociedad de la Información en la que los dispositivos electrónicos se harán una parte incorporada de la vida diaria encajados en casi cada producto. Es necesario un conocimiento cuidadoso de los desafíos para aprovechar la amplia gama de ocasiones ofrecidas por tales capacidades de integración y las correspondientes posibilidades de diseño de sistemas electrónicos From Specification to Embedded Systems Application Achim Rettberg, Mauro C. Zanella, Franz J. Rammig, 2005-08-10 IFIP TC10 Working Conference International Embedded Systems Symposium IESS August 15-17 2005 Manaus Brazil High-Level System Modeling Ronald Waxman, Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06

In system design generation of high level abstract models that can be closely associated with evolving lower level models provides designers with the ability to incrementally test an evolving design against a model of a specification. Such high level models may deal with areas such as performance, reliability, availability, maintainability, and system safety. Abstract models also allow exploration of the hardware versus software design space in an incremental fashion as a fuller detailed design unfolds, leaving behind the old practice of hardware/software binding too early in the design process. Such models may also allow the inclusion of non-functional aspects of design, e.g. space, power, heat, in a simulatable information model dealing with the system's operation. This book addresses Model Generation and Application specifically in the following domains: Specification modeling, linking object data modeling, behavior modeling, and activity modeling. Operational specification modeling: modeling the way the system is supposed to operate from a user's viewpoint. Linking non-functional parameters with specification models. Hybrid modeling: linking performance and functional elements. Application of high level modeling to hardware/software approaches. Mathematical analysis techniques related to the modeling approaches. Reliability modeling. Applications of High Level Modeling. Reducing High Level Modeling to Practice. High Level System Modeling. Specification and Design Methodologies describes the latest research and practice in the modeling of electronic systems and as such is an important update for all researchers, design engineers, and technical managers working in design automation and circuit design.

Algorithms and Architectures for Real-Time Control 1991 P.J. Fleming, D.I. Jones, 2014-07-22

Computer scientists have long appreciated that the relationship between algorithms and architecture is crucial. Broadly speaking, the more specialized the architecture is to a particular algorithm, then the more efficient will be the computation. The penalty is that the architecture will become useless for computing anything other than that algorithm. This message holds for the algorithms used in real-time automatic control as much as any other field. These Proceedings will provide researchers in this field with a useful up-to-date reference source of recent developments.

Specification and Verification of Concurrent Systems Charles Rattray, 2013-11-11

This volume

contains papers presented at the BCS FACS Workshop on Specification and Verification of Concurrent Systems held on 6-8 July 1988 at the University of Stirling Scotland. Specification and verification techniques are playing an increasingly important role in the design and production of practical concurrent systems. The wider application of these techniques serves to identify difficult problems that require new approaches to their solution and further developments in specification and verification. The Workshop aimed to capture this interplay by providing a forum for the exchange of the experience of academic and industrial experts in the field. Presentations included surveys, original research, practical experience with methods, tools, and environments in the following or related areas: Object oriented process data and logic based models and specification methods for concurrent systems; Verification of concurrent systems; Tools and environments for the analysis of concurrent systems; Applications of specification languages to practical concurrent system design and development. We should like to thank the invited speakers and all the authors of the papers whose work contributed to making the Workshop such a success. We were particularly pleased with the international response to our call for papers. Invited Speakers: Pierre America, Philips Research Laboratories, University of Warwick; Professor M. Joseph David, Freestone British Telecom; Organising Committee: Charles Rattray, Dr. Muffy Thomas, Dr. Simon Jones, Dr. John Cooke, Professor Ken Turner, Derek Coleman, Maurice Naftalin, Dr. Peter Scharbach. vi Preface. We would like to acknowledge the financial contribution made by SD Systems Designers, Camberley, Surrey.

UML'99 - The Unified Modeling Language. Beyond the Standard Robert B. France, Bernhard Rumpe, 1999-10-15. This book constitutes the refereed proceedings of the Second International Conference on the Unified Modeling Language UML 99 held in Fort Collins CO USA in September 1999. The 44 revised full papers presented together with two invited contributions and three panel summaries were carefully reviewed and selected from a total of 166 submissions. The papers are organized in topical sections on software architecture, UML and other notations formalizing interactions, meta modeling tools, components, UML extension mechanisms, process modeling, real time systems, constraint languages, analyzing UML models, precise behavioral modeling, applying UML, sequence design and coding.

Readings in Hardware/Software Co-Design Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002. This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s.

Model-Based Engineering of Embedded Real-Time Systems Holger Giese, Gabor Karsai, Edward A. Lee, Bernhard Rumpe, Bernhard Schätz, 2010-10-06. The topic of Model Based Engineering of Real Time Embedded Systems brings together a challenging problem domain, real time embedded systems, and a solution domain, model based engineering. It is also at the forefront of integrated software and systems engineering as software in this problem domain is an essential tool for system implementation and integration. Today, real time embedded software plays a crucial role in most advanced technical systems such as airplanes, mobile phones, and cars, and has become the main driver and catalyst for innovation. Development, evolution, verification, configuration, and maintenance of embedded and distributed software nowadays are often serious

challenges as drastic increases in complexity can be observed in practice Model based engineering in general and model based software development in particular advocates the notion of using models throughout the development and life cycle of an engineered system Model based software engineering reinforces this notion by promoting models not only as the tool of abstraction but also as the tool for verification implementation testing and maintenance The application of such model based engineering techniques to embedded real time systems appears to be a good candidate to tackle some of the problems arising in the problem domain

Applications and Theory of Petri Nets 2001 Jose-Manuel Colom,Maciej

Koutny,2003-05-15 This volume contains the proceedings of the 22nd International Conference on Application and Theory of Petri Nets The aim of the Petri net conferences is to create a forum for discussing progress in the application and theory of Petri nets Typically the conferences have 100 150 participants one third of these coming from industry while the rest are from universities and research institutions The conferences always take place in the last week of June This year the conference was organized jointly with the 2nd International Conference on Application of Concurrency to System Design ICACSD 2001 The two conferences shared the invited lectures and the social program The conference and a number of other activities are coordinated by a steering committee with the following members G Balbo Italy J Billington Australia G De Michelis Italy C Girault France K Jensen Denmark S magai Japan T Murata USA C A Petri Germany honorary member W Reisig Germany G Rozenberg The Netherlands chairman and M Silva Spain Other activities before and during the 2001 conference included tool demonstrations a meeting on XML Based Interchange Formats for Petri Nets intensive introductory tutorials two advanced tutorials on Probabilistic Methods in Concurrency and Model Checking and two workshops on Synthesis of Concurrent Systems and Concurrency in Dependable Computing The tutorial notes and workshop proceedings are not published in these proceedings but copies are available from the organizers

The Industrial Information Technology Handbook Richard Zurawski,2018-10-03 The Industrial Information Technology Handbook focuses on existing and emerging industrial applications of IT and on evolving trends that are driven by the needs of companies and by industry led consortia and organizations Emphasizing fast growing areas that have major impacts on industrial automation and enterprise integration the Handbook covers topics such as industrial communication technology sensors and embedded systems The book is organized into two parts Part 1 presents material covering new and quickly evolving aspects of IT Part 2 introduces cutting edge areas of industrial IT The Handbook presents material in the form of tutorials surveys and technology overviews combining fundamentals and advanced issues with articles grouped into sections for a cohesive and comprehensive presentation The text contains 112 contributed reports by industry experts from government companies at the forefront of development and some of the most renowned academic and research institutions worldwide Several of the reports on recent developments actual deployments and trends cover subject matter presented to the public for the first time

Languages, Compilers, and Tools for Embedded Systems Frank Mueller,Azer Bestavros,1998 This book constitutes the strictly refereed

post workshop proceedings of the ACM SIGPLAN Workshop on Languages Compilers and Tools for Embedded Systems LCTES 98 held in Montreal Canada in June 1998 The 19 revised papers presented were carefully reviewed and selected from a total of 54 submissions for inclusion in the book also included are one full paper and an abstract of an invited contribution The papers address all current aspects of research and development in the rapidly growing area of embedded systems and real time computing

Hardware Description Languages and their Applications Carlos Delgado Kloos, Eduard Cerny, 2013-06-05 In the past few decades Computer Hardware Description Languages CHDLs have been a rapidly expanding subject area due to a number of factors including the advancing complexity of digital electronics the increasing prevalence of generic and programmable components of software hardware and the migration of VLSI design to high level synthesis based on HDLs Currently the subject has reached the consolidation phase in which languages and standards are being increasingly used at the same time as the scope is being broadened to additional application areas This book presents the latest developments in this area and provides a forum from which readers can learn from the past and look forward to what the future holds

Models in System Design Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 Models in System Design tracks the general trend in electronics in terms of size complexity and difficulty of maintenance System design is by nature combined with prototyping mixed domain design and verification and it is no surprise that today s modeling and models are used in various levels of system design and verification In order to deal with constraints induced by volume and complexity new methods and techniques have been defined Models in System Design provides an overview of the latest modeling techniques for use by system designers The first part of the book considers system level design discussing such issues as abstraction performance and trade offs There is also a section on automating system design The second part of the book deals with some of the newest aspects of embedded system design These include co verification and prototyping Finally the book includes a section on the use of the MCSE methodology for hardware software co design Models in System Design will help designers and researchers to understand these latest techniques in system design and as such will be of interest to all involved in embedded system design

Formal Methods and Hybrid Real-Time Systems Cliff B.

Jones, Zhiming Liu, Jim Woodcock, 2007-09-04 This Festschrift volume is published to honour both Dines Bjørner and Zhou Chaochen on the occasion of their 70th birthdays The volume includes 25 refereed papers by leading researchers current and former colleagues who congregated at a celebratory symposium held in Macao China in the course of the International Colloquium on Theoretical Aspects of Computing ICTAC 2007 The papers cover a broad spectrum of subjects

Cybernetics And Systems '94 - Proceedings Of The 12th European Meeting On Cybernetics And Systems Research (In 2 Volumes) Robert Trappl, 1994-03-15 The papers in this volume reflect the most recent research findings in cybernetics and systems research They were selected from 298 draft final papers which were submitted to the conference by authors from more than 30 different countries from five continents

IEEE Circuits & Devices, 2004 A

Platform-Centric Approach to System-on-Chip (SOC) Design Vijay Madisetti, Chonlameth Arpnikanondt, 2006-06-28 Increasing system complexity has created a pressing need for better design tools and associated methodologies and languages for meeting the stringent time to market and cost constraints Platform centric and platfo based system on chip SoC design methodologies based on reuse of software and hardware functionality has also gained increasing exposure and usage within the Electronic System Level ESL design communities The book proposes a new methodology for realizing platform centric design of complex systems and presents a detailed plan for its implementation The proposed plan allows component vendors system integrators and product developers to collaborate effectively and efficiently to create complex products within budget and schedule constraints This book focuses more on the use of platforms in the design of products and not on the design of platforms themselves Platform centric design is not for everyone as some may feel that it does not allow them to differentiate their offering from competitors to a significant degree However its proponents may claim that the time market and cost advantages of platform centric design more than compensate for any drawbacks *Advances in Automotive Control 2004 (2-volume Set)* G Rizzo, L Glielmo, C Pianese, F Vasca, 2005-11-07

Unveiling the Energy of Verbal Art: An Psychological Sojourn through **Embedded Real Time Systems A Specification And Design Methodology**

In a global inundated with displays and the cacophony of instantaneous connection, the profound energy and emotional resonance of verbal beauty frequently disappear in to obscurity, eclipsed by the constant barrage of sound and distractions. Yet, situated within the lyrical pages of **Embedded Real Time Systems A Specification And Design Methodology**, a captivating perform of fictional splendor that pulses with organic feelings, lies an wonderful trip waiting to be embarked upon. Written with a virtuoso wordsmith, that mesmerizing opus instructions visitors on an emotional odyssey, delicately exposing the latent potential and profound affect stuck within the intricate web of language. Within the heart-wrenching expanse of this evocative analysis, we will embark upon an introspective exploration of the book is central themes, dissect their charming publishing model, and immerse ourselves in the indelible impact it leaves upon the depths of readers souls.

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