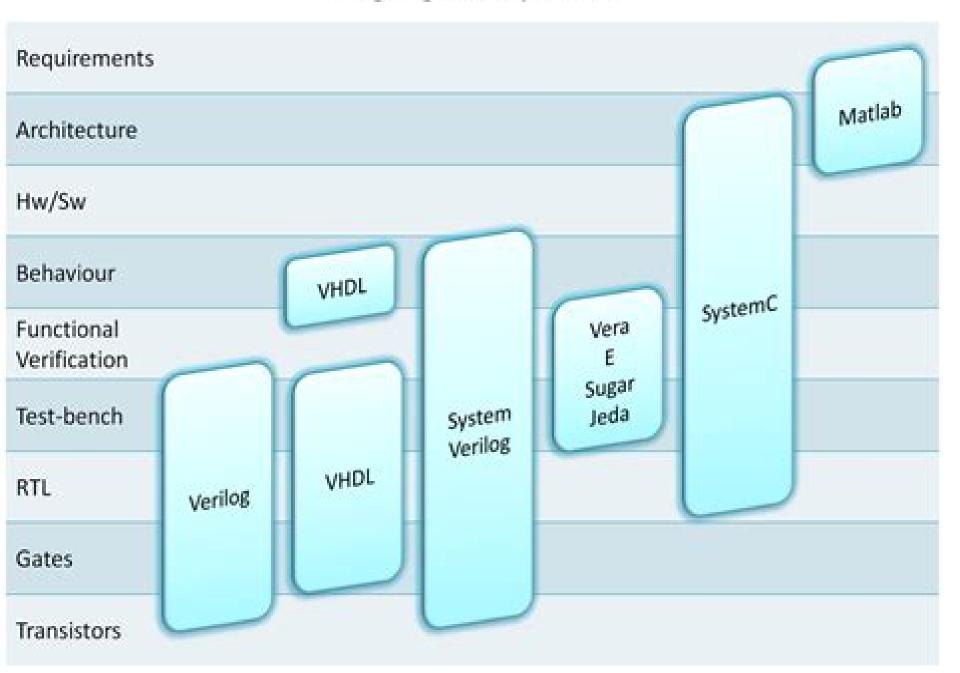
# Language Comparison



# **Hardware Description Languages**

Carlos Delgado Kloos, Eduard Cerny

#### **Hardware Description Languages:**

Fundamentals and Standards in Hardware Description Languages Jean Mermet, 2012-12-06 The second half of this century will remain as the era of proliferation of electronic computers They did exist before but they were mechanical During next century they may perform other mutations to become optical or molecular or even biological Actually all these aspects are only fancy dresses put on mathematical machines This was always recognized to be true in the domain of software where machine or high level languages are more or less rigourous but immaterial variations of the universaly accepted mathematical language aimed at specifying elementary operations functions algorithms and processes But even a mathematical machine needs a physical support and this is what hardware is all about The invention of hardware description languages HDL s in the early 60 s was an attempt to stay longer at an abstract level in the design process and to push the stage of physical implementation up to the moment when no more technology independant decisions can be taken It was also an answer to the continuous exponential growth of complexity of systems to be designed. This problem is common to hardware and software and may explain why the syntax of hardware description languages has followed with a reasonable delay of ten years the evolution of the programming languages at the end of the 60 s they were Algol like a decade later Pascal like and now they are C or ADA like They have also integrated the new concepts of advanced software specification Hardware Description Languages and their Applications Carlos Delgado Kloos, Eduard Cerny, 2013-06-05 In languages the past few decades Computer Hardware Description Languages CHDLs have been a rapidly expanding subject area due to a number of factors including the advancing complexity of digital electronics the increasing prevalence of generic and programmable components of software hardware and the migration of VLSI design to high level synthesis based on HDLs Currently the subject has reached the consolidation phase in which languages and standards are being increasingly used at the same time as the scope is being broadened to additional application areas This book presents the latest developments in this area and provides a forum from which readers can learn from the past and look forward to what the future holds

IEEE Standard Verilog Hardware Description Language ,2001 The Verilog Hardware Description Language HDL is defined in this standard Verilog HDL is a formal notation intended for use in all phases of the creation of electronic systems. Because it is both machine readable and human readable it supports the development verification synthesis and testing of hardware designs the communication of hardware design data and the maintenance modification and procurement of hardware The primary audiences for this standard are the implementors of tools supporting the language and advanced users of the language 

Computer Hardware Description Languages and their Applications D. Agnew, L. Claesen, R. Camposano, 2014-05-21 Hardware description languages HDLs have established themselves as one of the principal means of designing electronic systems. The interest in and usage of HDLs continues to spread rapidly driven by the increasing complexity of systems the growth of HDL driven synthesis the research on formal design methods and many other related

advances This research oriented publication aims to make a strong contribution to further developments in the field The following topics are explored in depth BDD based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification VHDL HDL based design methods high level synthesis and text graphical HDLs There are short papers covering advanced design capture and recent work in high level synthesis and formal verification In addition several invited presentations on key issues discuss and summarize recent advances in real time system design automatic verification of sequential circuits and languages for protocol specification 
Computer Hardware Description Languages and their Applications D. Borrione, R. Waxman, 2014-06-28 The topic areas presented within this volume focus on design environments and the applications of hardware description and modelling including simulation verification by correctness proofs synthesis and test The strong relationship between the topics of CHDL 91 and the work around the use and re standardization of the VHDL language is also explored The quality of this proceedings and its significance to the academic and professional worlds is assured by the excellent technical programme here compiled

Computer Hardware Description Languages and Their Applications John A. Darringer, F. J. Rammig, 1990 Languages for precisely describing the behavior of computers have been studied since the conception of the computer itself As the computer industry has grown so has the need to exchange information about computers Computer Hardware Description Languages CHDL are required to provide specifications and detailed implementation for a growing menu of sophisticated design automation tools including synthesis verification simulation analysis optimization placement wiring and testing communicate requirements and capabilities between suppliers and users of computer components and subsystems facilitate the transfer of new methods and results within the university and industrial research community As reflected in this book there is an increased emphasis on applications and resulting requirements that are placed on CHDLs In the field of synthesis a major application area there is today a focus on high level synthesis and synthesis under design constraints In the area of design correctness research has shifted away from simulation to formal verification techniques such as temporal logic

Analog and Mixed-Signal Hardware Description Language A. Vachoux, Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 Hardware description languages HDL such as VHDL and Verilog have found their way into almost every aspect of the design of digital hardware systems Since their inception they gradually proved to be an essential part of modern design methodologies and design automation tools ever exceeding their original goals of being description and simulation languages Their use for automatic synthesis formal proof and testing are good examples So far HDLs have been mainly dealing with digital systems However integrated systems designed today require more and more analog parts such as A D and D A converters phase locked loops current mirrors etc The verification of the complete system therefore asks for the use of a single language Using VHDL or Verilog to handle analog descriptions is possible as it is shown in this book but the real power is coming from true mixed signal HDLs that integrate discrete and continuous semantics into a unified framework

Analog HDLs AHDL are considered here a subset of mixed signal HDLs as they intend to provide the same level of features as HDLs do but with a scope limited to analog systems possibly with limited support of discrete semantics Analog and Mixed Signal Hardware Description Languages covers several aspects related to analog and mixed signal hardware description languages including The use of a digital HDL for the description and the simulation of analog systems The emergence of extensions of existing standard HDLs that provide true analog and mixed signal HDLs The use of analog and mixed signal HDLs for the development of behavioral models of analog electronic building blocks operational amplifier PLL and for the design of microsystems that do not only involve electronic parts The use of a front end tool that eases the description task with the help of a graphical paradigm yet generating AHDL descriptions automatically Analog and Mixed Signal Hardware Description Languages is the first book to show how to use these new hardware description languages in the design of electronic components and systems It is necessary reading for researchers and designers working in electronic design

VLSI Chip Design with the Hardware Description Language VERILOG Ulrich Golze, 2013-11-11 The art of transforming a circuit idea into a chip has changed permanently Formerly the electrical physical and geometrical tasks were predominant Later mainly net lists of gates had to be constructed Nowadays hardware description languages HDL similar to programming languages are central to digital circuit design HDL based design is the main subject of this book After emphasizing the economic importance of chip design as a key technology the book deals with VLSI design Very Large Scale Integration the design of modern RISC processors the hardware description language VERILOG and typical modeling techniques Numerous examples as well as a VERILOG training simulator are included on a disk The Verilog® Hardware Description Language Donald E. Thomas, Philip R. Moorby, 2013-03-14 XV Acknowledgments xvii Chapter 1 Verilog A Tutorial Introduction Getting Started 2 A Structural Description 2 Simulating the binaryToESeg Driver 4 Creating Ports For the Module 7 Creating a Testbench For a Module 8 Behavioral Modeling of Combinational Circuits II Procedural Models 12 Rules for Synthesizing Combinational Circuits 13 Behavioral Modeling of Clocked Sequential Circuits 14 Modeling Finite State Machines IS Rules for Synthesizing Sequential Systems 18 Non Blocking Assignment The Verilog® Hardware **Description Language** Donald Thomas, Philip Moorby, 2008-09-11 XV From the Old to the New xvii Acknowledgments xx Verilog A Tutorial Introduction Getting Started 2 A Structural Description 2 Simulating the binaryToESeg Driver 4 Creating Ports For the Module 7 Creating a Testbench For a Module 8 Behavioral Modeling of Combinational Circuits 11 Procedural Models 12 Rules for Synthesizing Combinational Circuits 13 Procedural Modeling of Clocked Sequential Circuits 14 Modeling Finite State Machines 15 Rules for Synthesizing Sequential Systems 18 Non Blocking Assignment A Comparison of Hardware Description Languages Ping Yeung, David Rees, 1991 Abstract In this report we compare the four hardware description languages HDL most widely available and used in industry They are ELLA Verilog VHDL and UDL I We concentrate on the specification aspect of the language Besides pointing out the differences attention is also paid to their

similarities A particularly close comparison is made of the two most commercial significant languages Verilog and VHDL In **Computer Hardware Description Languages and** the conclusion we look at what is still lacking in these languages Their Applications Mario R. Barbacci, Cees-Jan Koomen, 1987 The symposium on which this book is based has become established as the focal point for the meeting of experts in the field of formal descriptions of hardware and their use in analysis and synthesis of digital systems The papers reflect the gradual shift from the original emphasis on the uses of language design to describe hardware toward more formal techniques for specification and verification Design Using VHDL Pong P. Chu, 2006-04-20 The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient portable and scalable Register Transfer Level RTL digital circuits using the VHDL hardware description language and synthesis software Focusing on the module level design which is composed of functional units routing circuit and storage the book illustrates the relationship between the VHDL constructs and the underlying hardware components and shows how to develop codes that faithfully reflect the module level design and can be synthesized into efficient gate level implementation Several unique features distinguish the book Coding style that shows a clear relationship between VHDL constructs and hardware components Conceptual diagrams that illustrate the realization of VHDL codes Emphasis on the code reuse Practical examples that demonstrate and reinforce design concepts procedures and techniques Two chapters on realizing sequential algorithms in hardware Two chapters on scalable and parameterized designs and coding One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis it also examines the synthesis task from the perspective of the overall development process Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation verification and testing needs and can be easily incorporated into a larger system or reused Discussion is independent of technology and can be applied to both ASIC and FPGA devices With a balanced presentation of fundamentals and practical examples this is an excellent textbook for upper level undergraduate or graduate courses in advanced digital logic Engineers who need to make effective use of today s synthesis software and FPGA devices should also refer to this book IEEE Standard Description Language Based on the Verilog Hardware Description Language, 1364-1995, 1996

Hardware Description Languages Sumit Ghosh,2000 Hardware description languages HDLs hold the key to future processor designs but until now no book has offered a clear analysis of the basic principles underlying HDLs HARDWARE DESCRIPTION LANGUAGES is the first book to unlock the often hidden science of HDLs along with their origins and basic concepts This indispensable guide explains HDLs and includes an insightful overview of the foremost HDLs of the past three decades from Computer Design Language CDL to Very High Speed Integrated Circuit VHSIC to VHSIC Hardware Description Language VHDL To improve both your knowledge and digital designs of HDL fundamentals this valuable book features these essential topics A critical review of VHDL and Verilog Accurate modeling of hardware Distributed simulation of behavior

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International Conference on Simulation and Hardware Description Languages (ICSHDL) Philip A. Wilsey, David The Verilog® Hardware Description Language Donald E. Thomas, Philip R. Moorby, 2013-04-18 XVII Acknowledgments CHAPTER 1 Verilog A Tutorial Introduction 1 Getting Started 2 A Structural Description 2 Simulating the NAND Latch 4 Module Hleral Chy 6 The Counter 7 Components of the Counter 9 A Clock for the System 10 Tying the Whole Circuit Together 11 Using An Alternate Description of the Flip Flop 13 Behavioral Modeling 1 S A Behavioral Model of the m16 Counter 16 Mixing Structure and Behavior 18 Assignment Statements 22 Summary on Mixing Behavioral and Structural Descriptions 23 Creating a Testbench For a Module 24 Summary 2S Tutorial Guide to Formal Syntax Specification 26 Exercises 30 CHAPTER 2 Behavioral Modeling 33 Process Model 33 If Then Else 3S Where Does The ELSE Belong 39 The Conditional Operator 41 Loops 41 Four Basic Loop Statements 42 Exiting Loops on Exceptional Conditions 45 Multi way branching 46 If Else If 46 Case 46 Comparison of Case and If Else If 48 viii The Verilog Hardware Description Language Casez and Casex 49 Functions and Tasks SO Tasks 52 Functions 55 A Structural View 57 Rules of Scope and Hierarchical Names S9 Rules of Scope 60 Hierarchical Names 62 Summary 63 Exerdses 63 CHAPTER 3 Concurrent Processes 6S Concuent Processes 6S Events 67 Event Control Statement 67 Named Events 69 The Walt Statement 72 A Complete Producer Consumer Handshake 74 Comparison of the Wait and While Statements 77 Comparison of Wait and Event Control Statements 78 A Concuent Process Example 78 Disabling Named Blocks 84 Intra Assignment Control and Timing Events 87 Procedural Continuous Assignment 90 Computer Hardware Description Languages and Their Applications Dominique Borrione, Ron Waxman, 1991 The topic areas presented within this volume focus on design environments and the applications of hardware description and modelling including simulation verification by correctness proofs synthesis and test The strong relationship between the topics of CHDL 91 and the work around the use and re standardization of the VHDL language is also explored The quality of this proceedings and its significance to the academic and professional worlds is assured by the excellent technical programme here compiled Compilation, Synthesis, and Simulation of Hardware Description Languages: the Compositional Models of HDL's Szu-Tsung Cheng, 1998

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