# FPGA design of a camera control system for road traffic monitoring

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Abstract Video surveillance and monitoring more than a decade now remains a big challenge. Today is becoming more and more vital due to rapid development of hardware equipments as well as the software methods that support intelligent monitoring and analytics. This paper proposes the architecture of a FPGA design of a camera controller for surveillance that could be applied for road traffic monitoring and image data management purposes. The proposed system could deal with real-time monitoring of persistent and transient objects within the specific environment of a motorway or crossroad, taking input from a camera. Functional models were synthesized in VHDL. The design and simulations analysis were carried out within Quartus II Altera software package while further analysis was also performed with ModelSim. The functional performance and operation of the proposed design was evaluated based on such simulations and analysis.

Keywords monitoring; camera controller; FPGA design; simulations

#### I. INTRODUCTION

Video surveillance systems that use analog CCTV cameras and interfaces as the basis are not easily expandable, and have low video resolution with little or no signal processing. However, next generation of video surveillance systems with digital LAN cameras, that support complex image processing, and video-over-IP routing are more intelligent and allow further scalability and flexibility [6], [7], [8]. In this direction, programmable modules, with or without embedded DSP blocks, with memory blocks, several interfaces, and other units, fulfill the new contemporary system requirements [5], [12].

Today several systems are designed on the same chip, based on a Field Programmable Gate Array (FPGA), for intelligent and fast data processing and miniaturization of the system. Particularly, in modern surveillance systems, FPGA-based intelligent controllers play an important role in improving the performance of the camera control system [2], [3], [4], [16], [19].

The purpose of this paper is to present first results of an ongoing research project on the implementation of a smart camera control system for traffic surveillance based on a FPGA. The problem of providing automated vision surveillance on an efficient way that last years has been intensively examined [10], [11], [13]. Today there is a rapid development of such systems towards intelligent control units [1], [9], [15] and several algorithms have been proposed towards this direction [14], [17].

In this research, the system proposed captures a video stream, computes traffic information and performs on chip video analytics and finally transfers the video stream and the traffic information to a host PC in order to be generated further alarm signals and events to be triggered according to various traffic situations. The objective of the paper is to describe the design of an intelligent surveillance system within an academic environment for traffic monitoring purposes. The proposed architecture incorporates a camera controller that obtains traffic images and performs analytics. It performs contour identification, axis specification, axis shift, percent zoom, smooth, etc., as well as facilities for mathematical and statistical analysis of the numerical data (pedestrians, cars, etc.), such as densities, deviations, aggregates, etc.

In section II is described the design methodology followed and the goals aimed to be achieved. In section III the system architecture is detailed and section IV discusses the simulations carried towards the evaluation of system's functionality. Finally, section V presents a brief discussion and conclusions on the proposed system.

#### II. DESIGN METHODOLOGY AND GOALS

#### A. Denign Approach

The overall design of the IP surveillance camera controller is to be implemented as a complete system on a chip using a FPGA device. An FPGA-based architecture is quite flexible and customizable, to such extend that easily allows a system architecture to be enhanced and modified according to various system specifications and requirements. Thus FPGA architectures are easily reconfigurable.

In addition, in our case study of remote traffic monitoring would be possible to add custom video processing software in real time (video analytics) and system upgrades (i.e. a new

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in December 2000 The scope of the Application papers has expanded over recent years to cover not just innovative applications using traditional knowledge based systems but also to include applications demonstrating the whole range of AI technologies This volume contains thirteen refereed papers describing deployed applications or emerging applications together with an invited keynote paper by Dr Daniel Clancy of NASA Ames Research Centre The papers were subject to refereeing by at least two expert referees All papers which were controversial for some reason were discussed in depth by the Application Programme Committee For the application stream a paper is acceptable even if it describes a system which has not yet been installed provided the application is original and the paper discusses the kinds of things that would help others needing to solve a similar problem Papers have been selected to highlight critical areas of success and failure and to present the benefits and lessons learnt to other developers Papers this year cover topics as diverse as KBS for maintaining offshore platforms Data Mining to predict corporate business failure integrated AI techniques to support field service engineers Natural Language applied to the Data Protection Act knowledge management and the application of neural IEEE International Conference on Intelligent Transportation Systems Proceedings ,2003 networks 3rd EAI International Conference on Big Data Innovation for Sustainable Cognitive Computing Anandakumar Haldorai, Arulmurugan Ramu, Sudha Mohanram, Joan Lu, 2022-01-01 This book features the proceedings of The EAI International Conference on Big Data Innovation for Sustainable Cognitive Computing BDCC 2020 which took place 18 19 December 2020 The papers feature detail on cognitive computing and its self learning systems that use data mining pattern recognition and natural language processing NLP to mirror the way the human brain works This international conference focuses on technologies from knowledge representation techniques and natural language processing algorithms to dynamic learning approaches Topics covered include Data Science for Cognitive Analysis Real Time Ubiquitous Data Science Platform for Privacy Preserving Data Science and Internet Based Cognitive Platform **Challenges and Solutions for Sustainable** Smart City Development R. Maheswar, M. Balasaraswathi, Ravi Rastoqi, A. Sampathkumar, G. R. Kanagachidambaresan, 2021-05-22 This book discusses advances in smart and sustainable development of smart environments. The authors discuss the challenges faced in developing sustainable smart applications and provide potential solutions The solutions are aimed at improving reliability and security with the goal of affordability safety and durability Topics include health care applications sustainable smart transportation systems intelligent sustainable wearable electronics and sustainable smart building and alert systems Authors are from both industry and academia and present research from around the world Addresses problems and solutions for sustainable development of smart cities Includes applications such as healthcare transportation wearables security and more Relevant for scientist and researchers working on real time smart city development Big Data Analytics and Data Science Vikrant Bhateja, Hong Lin, Milan Simic, Jinshan Tang, Vustikayala Sivakumar Reddy, 2024-12-23 This book presents a collection of high quality peer reviewed research papers from the 8th

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